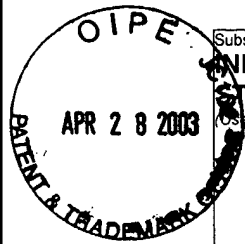


Paper # 21



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	09/473,315
Filing Date	December 28, 1999
First Named Inventor	Mosley, Larry
Group Art Unit	2831
Examiner Name	Thomas, Eric

Sheet 1 of 1

Attorney Docket No: 884.209US1

PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
T	US-5,530,288	06/25/1996	Stone, David B.	257	700	10/12/1994
m	US-6,037,044	03/14/2000	Giri, Ajay P., et al.	428	209	01/08/1998
/	US-6,272,020	08/07/2001	Tosaki, Hiromi, et al.	361	763	10/15/1998

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
--------------------	---------------------	------------------	---	-------	----------	----------------

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
✓		"Capacitor for Multichip Modules", IBM Technical Disclosure Bulletin, vo. 20, issue 8, (01/01/1978), 3117-3118	

EXAMINER

DATE CONSIDERED

5/22/03